

Amendments to the Specification:

Please replace paragraph [0035] (published as paragraph [0037]) with the amended paragraph:

[0035] Referring now to FIG. 3, there is shown a block diagram of an apparatus 300 according to one aspect of the invention. The apparatus 300 may be the traffic generator 110, the traffic receiver 120 (FIG. 1), the network cards 114, 124, or one or more components of the traffic generator 110 and the traffic receiver 120 or the network cards 114, 124, such as a port. The apparatus 300 includes a control unit 310, a blaster unit 340, a receive engine 320, a front end/transmit engine 350, a bus 330, a control line 360 and a back plane 370. The control unit 310 may include a port processor 312, a DMA (direct memory access) engine 314, and a port memory 316. The control unit 310, the blaster unit 340, the receive engine 320 and the front end/transmit engine 350 may be hardware, software, firmware, or a combination thereof. Additional and fewer units, modules or other arrangement of software, hardware and data structures may be used to achieve the apparatus 300.

Please replace paragraph [0064] (published as paragraph [0070]) with the amended paragraph:

[0064] The traffic generator 110 is configured to provide a simulated encrypting gateway 520. In an IP environment, the simulated encrypting gateway 520 has an IP address configured as protecting the simulated generating subnet 510. Thus, generated traffic 515 from the simulated generating subnet 510 goes to the simulated encrypting gateway 520. The simulated encrypting gateway 520 encrypts the traffic, encapsulates the encrypted traffic to go to the real gateway 150+60 and transmits the encapsulated traffic 525 to the address of the real gateway 150. However, the encapsulated traffic 525 does not go directly to the real gateway 150+60.